

We have an immediate open position for a postdoctoral researcher (project research associate) in the field of IC chip design and with the following background:

PhD in Electrical Engineering

Research topic: Gate driver and sensor IC design for power electronics

Experience with design and measurement of analog IC chip is required.

Experience with (1), (2) or (3) is a plus.

(1) Switching measurement of power devices.

(2) Gate driver for power transistors.

(3) Sensing in power electronics.

Please send your CV to mtaka@iis.u-tokyo.ac.jp. Please use the email subject of "Postdoc Applicant " (application deadline: January 21, 2022).