Energy Efficient Design and Energy Harvesting for Energy Autonomous Systems

Makoto Takamiya University of Tokyo Tokyo, Japan

Abstract— Energy autonomy enabled by the energy efficient design and the energy harvesting is the one of the top requirements for maintenance-free IoT sensor nodes and wearable/implanted devices. In this paper, energy efficient ultralow voltage (< 0.5V) circuits are shown. Energy autonomous wearable healthcare devices using the flexible, large-area, and distributed organic electronics are also shown.

I. INTRODUCTION

Requirements for IoT sensor nodes, wearable healthcare devices, and implanted medical devices are the wearingunconsciousness and the maintenance-free operation as shown in Fig.1. To enable the wearing-unconsciousness, mechanically flexible or small-size devices with the wireless connection are required. To enable the maintenance-free operation, energy autonomous devices are required. The energy autonomy is achieved by both the energy efficient operation and the energy harvesting. In this paper, energy autonomous systems with the energy efficient design and the energy harvesting are shown.

II. ENERGY EFFICIENT DESIGN

The energy efficient operation is achieved by a nearthreshold operation and a temporal-spatial fine-grained control. Fig. 2 shows the simulated power supply voltage (V_{DD}) dependence of power, delay, and energy. Compared with the nominal operation at V_{DD} = 1.2V, the energy of the nearthreshold operation at V_{DD} = 0.3V is reduced to one-tenth of that at V_{DD} = 1.2V. Fig. 3 shows the temporal-spatial finegrained control [1]. In the conventional design, the clock frequency (f_{CLK}), V_{DD}, and the threshold voltage (V_{TH}) of transistors are common within a chip. In contrast, in the state-

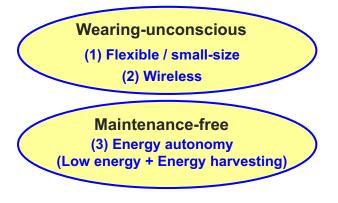


Fig. 1. Requirements for wearable/implanted devices.

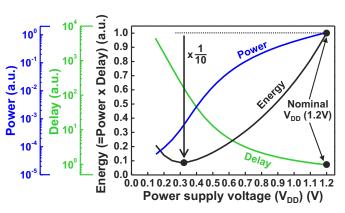


Fig. 2. Power supply voltage dependence of power, delay, and energy.

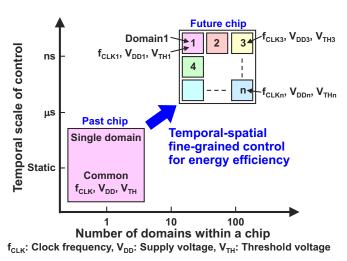
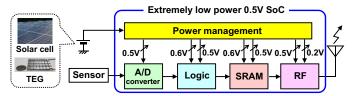


Fig. 3. Temporal-spatial fine-grained control.

of-the-art design, many different f_{CLK} 's, V_{DD} 's, and V_{TH} 's are used within a chip and they are dynamically changed to minimize the energy. Such temporal-spatial fine-grained control includes the dynamic voltage scaling [2], the dynamic frequency scaling [3], the power gating, the clock gating, the body biasing, the local gate overdrive [4], and the quick wakeup circuits [5].

Fig. 4 shows a block diagram of extremely low power 0.5V SoC for IoT sensor nodes. The voltage obtained from the energy harvester is regulated by the power management circuits [6-7] and many different V_{DD}'s are given to A/D



TEG: Thermoelectric generator

Fig. 4. Block diagram of extremely low power 0.5V SoC for IoT sensor nodes.

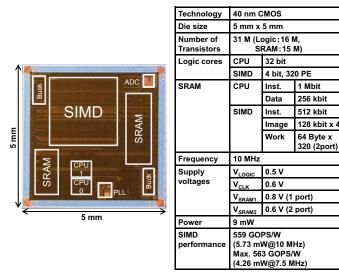


Fig. 5. 0.5V image processor [3].

converter, logic circuits, SRAM, and RF circuits for the temporal-spatial fine-grained control to minimize the energy. Fig. 5 shows an example of 0.5V SoC [3]. The 0.5V image processor with 563 GOPS/W SIMD and 32bit CPU includes an all digital PLL (ADPLL), an A/D converter, and buck converters.

III. ENERGY AUTONOMOUS WEARABLE FLEXIBLE DEVICES

The organic electronics enables flexible, large-area, and distributed sensor and/or actuator array and is suitable for the wearing-unconscious devices. Several energy autonomous wearable healthcare devices using the organic electronics have been proposed [8-9].

Fig. 6 shows a photograph of a fever alarm armband (FAA) [10] integrating fully flexible solar cells, a piezoelectric speaker, a temperature detector, and 12V organic complementary FET circuits. FAA is a flexible energy autonomous healthcare device with the wireless interface. FAA is looped around an upper arm of a patient in a hospital room, and the temperature detector monitors the underarm temperature of the patient. 220-µm thickness amorphous silicon solar cells attached outside of the upper arm generate the power. When high fever is detected, a 52-µm thickness piezoelectric speaker with polyvinylidene difluoride (PVDF) makes a sound to alarm a nurse. Organic circuits and the temperature detector are fabricated on a 50-µm thickness flexible polyimide film, and the solar cells and the speaker are attached on it. Fig. 7 shows a block diagram of FAA. An active

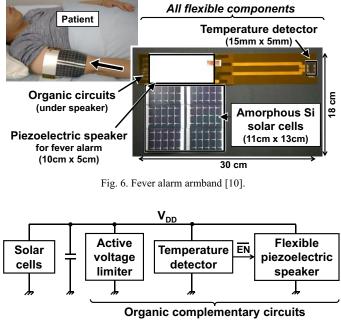


Fig. 7. Block diagram of fever alarm armband [10].

voltage limiter regulates V_{DD} . When the measured temperature is higher than the preset threshold temperature, a ring oscillator starts oscillation and the speaker makes a sound.

ACKNOWLEDGMENT

This work is supported in part by JST/ERATO.

REFERENCES

- [1] T. Yasufuku, K. Hirairi, Y. Pu, Y. -F. Zheng, R. Takahashi, M. Sasaki, H. Fuketa, A. Muramatsu, M. Nomura, F. Shinohara, M. Takamiya, and T. Sakurai, "24% Power Reduction by Post-Fabrication Dual Supply Voltage Control of 64 Voltage Domains in VDDmin Limited Ultra Low Voltage Logic Circuits," IEEE International Symposium on Quality Electronic Design, pp. 586-591, March 2012.
- [2] K. Hirairi, Y. Okuma, H. Fuketa, T. Yasufuku, M. Takamiya, M. Nomura, H. Shinohara, and T. Sakurai, "13% Power Reduction in 16b Integer Unit in 40nm CMOS by Adaptive Power Supply Voltage Control with Parity-Based Error Prediction and Detection (PEPD) and Fully Integrated Digital LDO," IEEE International Solid-State Circuits Conference, pp. 486-487, Feb. 2012.
- [3] M. Nomura, A. Muramatsu, H. Takeno, S. Hattori, D. Ogawa, M. Nasu, K. Hirairi, S. Kumashiro, S. Moriwaki, Y. Yamamoto, S. Miyano, Y. Hiraku, I. Hayashi, K. Yoshioka, A. Shikata, H. Ishikuro, M. Ahn, Y. Okuma, X. Zhang, Y. Ryu, K. Ishida, M. Takamiya, T. Kuroda, H. Shinohara, and T. Sakurai, "0.5V Image Processor with 563 GOPS/W SIMD and 32bit CPU Using High Voltage Clock Distribution (HVCD) and Adaptive Frequency Scaling (AFS) with 40nm CMOS," IEEE Symposium on VLSI Circuits, pp. C36-C37, June 2013.
- [4] X. Zhang, P. -H. Chen, Y. Ryu, K. Ishida, Y. Okuma, K. Watanabe, T. Sakurai, and M. Takamiya, "A 0.45-V Input On-Chip Gate Boosted (OGB) Buck Converter in 40-nm CMOS with More Than 90% Efficiency in Load Range from 2µW to 50µW," IEEE Symposium on VLSI Circuits, pp. 194-195, June 2012.
- [5] S. Iguchi, H. Fuketa, T. Sakurai, and M. Takamiya, "92% Start-up Time Reduction by Variation-Tolerant Chirp Injection (CI) and Negative Resistance Booster (NRB) in 39MHz Crystal Oscillator," IEEE Symposium on VLSI Circuits, pp. 236-237, June 2014.

- [6] P. -H. Chen, K. Ishida, K. Ikeuchi, X. Zhang, K. Honda, Y. Okuma, Y. Ryu, M. Takamiya, and T. Sakurai, "Startup Techniques for 95 mV Step-Up Converter by Capacitor Pass-On Scheme and Vth-Tuned Oscillator With Fixed Charge Programming," IEEE Journal of Solid-State Circuits, Vol.47, No.5, pp. 1252-1260, May. 2012.
- [7] P. -H. Chen, X. Zhang, K. Ishida, Y. Okuma, Y. Ryu, M. Takamiya, and T. Sakurai, "An 80 mV Startup Dual-Mode Boost Converter by Charge-Pumped Pulse Generator and Threshold Voltage Tuned Oscillator With Hot Carrier Injection," IEEE Journal of Solid-State Circuits, Vol. 47, No. 11, pp. 2554-2562, Nov. 2012.
- [8] K. Ishida, T. -C. Huang, K. Honda, Y. Shinozuka, H. Fuketa, T. Yokota, U. Zschieschang, H. Klauk, G. Tortissier, T. Sekitani, M. Takamiya, H. Toshiyoshi, T. Someya, and T. Sakurai, "Insole Pedometer With Piezoelectric Energy Harvester and 2V Organic Digital and Analog

Circuits," IEEE International Solid-State Circuits Conference, pp. 308-309, Feb. 2012.

- [9] H. Fuketa, K. Yoshioka, T. Yokota, W. Yukita, M. Koizumi, M. Sekino, T. Sekitani, M. Takamiya, T. Someya, and T. Sakurai, "Organic-Transistor-Based 2kV ESD-Tolerant Flexible Wet Sensor Sheet for Biomedical Applications with Wireless Power and Data Transmission Using 13.56MHz Magnetic Resonance," IEEE International Solid-State Circuits Conference, pp. 490-491, Feb. 2014.
- [10] H. Fuketa, M. Hamamatsu, T. Yokota, W. Yukita, T. Someya, T. Sekitani, M Takamiya, T. Someya, and T. Sakurai, "Energy Autonomous Fever Alarm Armband Integrating Fully Flexible Solar Cells, Piezoelectric Speaker, Temperature Detector, and 12V Organic Complementary FET Circuits," IEEE International Solid-State Circuits Conference, to be presented, Feb. 2015.