Modeling of 3-Level Buck Converters in Discontinuous Conduction Mode for Stand-by Mode Power Supply

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Abstract— A 3-level buck converter in the discontinuous conduction mode (DCM) is a key circuit for integrated voltage regulators to achieve high efficiency at a light load in the stand-by mode operation of microprocessors. In this paper, fundamental circuit characteristics including the conversion ratio and transfer function are derived for the first time, enabling the design of controllers for 3-level buck converters in the DCM. The derived transfer function is verified by time-domain small-signal-injection simulation. Similar to conventional 2-level buck converters, 3-level buck converters in the DCM have a first-order lag transfer function, while 3-level buck converters in the continuous conduction mode (CCM) have a second-order lag transfer function, suggesting that different controllers are required for the DCM and CCM.

I. INTRODUCTION

To enable the energy-efficient operation of many-core microprocessors, fine-grained per-core dynamic voltage scaling is required. As the number of cores increases, the required number of power supply voltages (V_{DD}) also increases. Integrated voltage regulators (IVRs) are a means of generating multiple V_{DD} on a chip, because increasing the number of off-chip voltage regulators is not practical. The key requirements of IVRs are a high power conversion efficiency (η) and a small form factor.

As shown in Fig. 1, a 3-level buck converter [1-6] combining a cascode 2-level buck converter [7] and a 1/2 switched capacitor DC-DC converter is a key circuit for IVRs, because the 3-level buck converter has a smaller inductance and faster transient response than the conventional 2-level buck converter at fixed ripples [2,5,6]. A fully integrated 3-level buck converter in the continuous conduction mode (CCM) delivering a current of 150mA with η of 72% for the active mode operation of microprocessors has been reported [2]. The converter, however, cannot be applied to the stand-by mode operation of microprocessors because η is low at a light load (e.g., < 1mA) owing to the CCM. In IVRs for energy-efficient microprocessors, a high η in the stand-by mode as well as the active mode is required. To achieve a high η at a light load, discontinuous conduction mode (DCM) operation is required. However, there have been very few publications of 3-level buck converters in the DCM [4], although a detailed analysis of a 3level buck converter in the CCM was reported in [5,6]. Reference [4] does not include an analysis on the fundamental circuit characteristics of a 3-level buck converter in the DCM, meaning that it is not yet clarify to design controllers.

To solve this problem, in this paper, fundamental circuit characteristics including the conversion ratio and transfer



Fig. 1. Power stage and output filters of cascade 2-level buck converter, $1\!/\!2$ switched capacitor DC-DC converter, and 3-level buck converter.

function of 3-level buck converters in the DCM are derived for the first time, enabling the design of controllers. In Section II, the circuit operation of 3-level buck converters in the DCM is shown. A detailed derivation of circuit characteristics including the transfer function in the DCM is given in Section III, and in Section IV, a time-domain small-signal-injection simulation validates the derived results. Finally, Section V concludes this paper.

II. CIRCUIT OPERATION OF 3-LEVEL BUCK CONVERTERS IN DISCONTINUOUS CONDUCTION MODE

The 3-level buck converter in Fig. 1 includes four switches $(P_1, P_2, N_1, \text{ and } N_2)$. As shown in Fig. 2, the 3-level buck converter in the DCM has 5 states (S_1-S_5) , instead of the 4 states (S_1-S_4) in the 3-level buck converter in the CCM [3]. S_5 only exists in the DCM and the inductor current (I_L) is zero in the S_5 state. Fig. 3 shows operation waveforms of the voltage in the V_X node and I_L in the 3-level buck converter in the DCM. T_S is the duration of one period and D_1-D_6 are duty ratios. D_1, D_2 , and D_3 are assumed to be equal to D_4, D_5 , and D_6 , respectively. Note that the voltage across the flying capacitor (V_{CFLY}) is assumed to be half of V_{IN} in the case of a sufficiently large C_{FLY} . When the conversion ratio $M (= V_{OUT}/V_{IN})$ is less than 1/2, the state transition order is $(S_1, S_2, S_5, S_3, S_2, S_5)$. In contrast, when M is above 1/2, the order is $(S_4, S_1, S_5, S_4, S_3, S_5)$.





Fig. 3. Switching patterns of 3-level buck converter. (a) $M \le 0.5$, (b) $M \ge 0.5$.

III. CIRCUIT CHARACTERISTICS OF 3-LEVEL BUCK CONVERTERS IN DISCONTINUOUS CONDUCTION MODE

A. Analysis of Conversion Ratio

The conversion ratio M of a 3-level buck converter in the DCM is derived by combining the volt-second balance in the inductor voltage (V_L), and the balance of the load current and the averaged I_L .

$$\left\langle V_{\rm L} \right\rangle = 0 \tag{1}$$

$$\frac{V_{\rm OUT}}{R_{\rm OUT}} = \langle I_{\rm L} \rangle \tag{2}$$

When $M \leq 0.5$, (1) is expressed as

$$D = (V_{\rm IN} - V_{\rm CF} - V_{\rm OUT}) D_1 T_{\rm S} + (0 - V_{\rm OUT}) D_2 T_{\rm S} + 0 \cdot D_3 T_{\rm S} + (V_{\rm CF} - V_{\rm OUT}) D_4 T_{\rm S} + (0 - V_{\rm OUT}) D_5 T_{\rm S} + 0 \cdot D_6 T_{\rm S} \Leftrightarrow \frac{V_{\rm OUT}}{V_{\rm IN}} = \frac{1}{2} \times \frac{D_1}{D_1 + D_2},$$
(3)

and (2) becomes

$$\frac{V_{\rm OUT}}{R_{\rm OUT}} = \frac{1}{2} I_{\rm PK} (D_{\rm I} + D_{\rm 2}) \times 2$$

$$= \frac{1}{2} V_{\rm IN} - V_{\rm OUT}}{I} D_{\rm I} (D_{\rm I} + D_{\rm 2}) T_{\rm s}.$$
(4)

By pairing (3) and (4), a quadratic equation for M is acquired, $D^2 = D^2$

$$0 = M^2 + \frac{D_1}{K}M - \frac{D_1}{2K},$$
(5)

where K is defined as

$$K = \frac{2L}{R_{\rm OUT}T_{\rm s}}.$$
 (6)

By solving (6), taking the positive sign, M is derived as

$$M = \frac{1}{1 + \sqrt{1 + 2\left(\frac{K}{D_1^2}\right)}}.$$
 (7)

When $M \ge 0.5$, (3)–(5), and (7) correspond to (8)–(11), respectively.

$$\frac{V_{\rm OUT}}{V_{\rm IN}} = \frac{D_1 + (D_1 + D_2)}{2(D_1 + D_2)}$$
(8)

$$\frac{V_{\text{OUT}}}{V_{\text{IN}}} = \left(1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}}\right) \frac{R_{\text{OUT}}T_{\text{s}}}{L} D_1 \left(D_1 + D_2\right)$$
(9)

$$0 = M^{2} - \frac{1}{2} \left(1 - \frac{2D_{1}^{2}}{K} \right) M - \frac{D_{1}^{2}}{K}$$
(10)

$$M = \frac{2}{1 - \frac{K}{2D_{1}^{2}} + \sqrt{\left(1 - \frac{K}{2D_{1}^{2}}\right)^{2} + \frac{4K}{D_{1}^{2}}}$$
(11)

Note that (7) and (11) are expressed in terms of only the control parameter D_1 and circuit parameters such as L, T_S , and R_{OUT} .

B. Analysis of Transfer Function with Averaged Switch Model

The transfer functions of a 3-level buck converter in the DCM are derived by using an averaged switch model of switch networks [8]. Fig. 4 shows the switch network in a 3-level buck converter in the DCM. The voltage and current waveforms corresponding to Fig. 4 are illustrated in Fig. 5. The averaged switch network quantities, $\langle V_1 \rangle$, $\langle V_2 \rangle$, $\langle I_1 \rangle$, and $\langle I_2 \rangle$, are calculated as follows: When $M \leq 0.5$,

$$\begin{cases} \langle V_1 \rangle = V_{\rm IN} \\ \langle V_2 \rangle = V_{\rm OUT} \\ \langle I_1 \rangle = \frac{D_1^2 T_{\rm S}}{2L} \left(\frac{V_{\rm IN}}{2} - V_{\rm OUT} \right) \\ \langle I_2 \rangle = \frac{D_1^2 T_{\rm S}}{2L} \cdot \frac{V_{\rm IN}}{V_{\rm OUT}} \left(\frac{V_{\rm IN}}{2} - V_{\rm OUT} \right), \end{cases}$$
(12)

and when $M \ge 0.5$,

$$\begin{cases} \langle V_1 \rangle = V_{\rm IN} \\ \langle V_2 \rangle = V_{\rm OUT} \\ \langle I_1 \rangle = \frac{D_1^2 T_{\rm S}}{L} \frac{V_{\rm OUT} \left(V_{\rm IN} - V_{\rm OUT} \right)}{2V_{\rm OUT} - V_{\rm IN}} \\ \langle I_2 \rangle = \frac{D_1^2 T_{\rm S}}{L} \frac{V_{\rm IN} \left(V_{\rm IN} - V_{\rm OUT} \right)}{2V_{\rm OUT} - V_{\rm IN}}. \end{cases}$$
(13)

Fig. 6 shows the small-signal equivalent circuit of Fig. 4. For small signals, the equivalent circuit is obtained by linearization of the expressions for the averaged switch models. Note that v_1 , v_2 , i_1 , i_2 , and d are small perturbations from $\langle V_1 \rangle$, $\langle V_2 \rangle$, $\langle I_1 \rangle$, $\langle I_2 \rangle$, and D_1 , respectively. The parameters $(j_1, g_1, r_1, j_2, g_2, and r_2)$ in Fig. 6 are summarized in Table I.

From the small-signal equivalent model, a control-to-output transfer function can be found as follows:

$$G_{vd}(s) = \frac{v_{\text{OUT}}}{d}\Big|_{v_{\text{IN}}=0} = \frac{G_{d0}}{1+s/\omega_p}$$
(14)



Fig. 4. 3-level buck converter with definition of switch network terminal quantities, V_1 , V_2 , I_1 , and I_2 .





Fig. 6. Small-signal ac models of 3-level buck converter in DCM obtained by averaged switch model.

Fig. 5. Voltage and current waveforms of switch network. (a) $M \le 0.5$, (b) $M \ge 0.5$.

Table I. Summary of parameters in Fig. 6

3-Level Buck Converter	$j_1 \equiv \frac{\partial \left\langle i_1 \right\rangle}{\partial D_1}$	$r_{\rm l} \equiv 1 \Big/ \frac{\partial \left< i_{\rm l} \right>}{\partial V_{\rm IN}}$	$g_1 \equiv -\frac{\partial \langle i_1 \rangle}{\partial V_{\text{OUT}}}$	$j_2 \equiv \frac{\partial \langle i_2 \rangle}{\partial D_1}$	$r_2 \equiv -1 / \frac{\partial \langle i_2 \rangle}{\partial V_{\text{OUT}}}$	$g_2 \equiv \frac{\partial \langle i_2 \rangle}{\partial V_{\text{IN}}}$
$0 \le M \le 0.5$	$j_1 = \frac{2D_1 V_{\rm IN}}{KR_{\rm OUT}} \left(\frac{1}{2} - M\right)$	$r_1 = \frac{2KR_{\text{OUT}}}{D_1^2}$	$g_1 = \frac{D_1^2}{KR_{\text{OUT}}}$	$j_2 = \frac{D_1 V_{\rm IN}}{KR_{\rm OUT}} \cdot \frac{1 - 2M}{M}$	$r_2 = \frac{2KR_{\text{OUT}}}{D_1^2}M^2$	$g_2 = \frac{D_1^2}{KR_{\text{OUT}}} \cdot \frac{1-M}{M}$
$0.5 \le M \le 1.0$	$j_{1} = \frac{2D_{1}V_{\text{IN}}}{KR_{\text{OUT}}} \cdot \frac{2M(1-M)}{2M-1}$	$r_{\rm l} = \frac{KR_{\rm OUT}}{2D_{\rm l}^2} \frac{(2M-1)^2}{M^2}$	$g_{1} = \frac{D_{1}^{2}}{KR_{\text{OUT}}} \cdot \frac{(2M-1)^{2}+1}{(2M-1)^{2}}$	$j_2 = \frac{2D_1 V_{\rm IN}}{KR_{\rm OUT}} \cdot \frac{2(1-M)}{2M-1}$	$r_2 = \frac{KR_{\rm OUT}}{2D_1^2} (2M - 1)^2$	$g_{2} = \frac{2D_{1}^{2}}{KR_{\text{OUT}}} \cdot \frac{1 - 2(M - 1)^{2}}{(2M - 1)^{2}}$

Table II. Values of M and control-to-output transfer function $G_{vd}(s)$ of 2-level and 3-level buck converters in DCM

	2-Level DCM Buck Converter	3-Level DCM Buck Converter (This Work)		
	[8]	<i>M</i> <0.5	<i>M</i> >0.5	
$M = V_{\rm OUT} / V_{\rm IN}$	$\frac{2}{1+\sqrt{1+4\left(\frac{K}{D_{1}^{2}}\right)}}$	$\frac{1}{1+\sqrt{1+2\left(\frac{K}{D_1^2}\right)}}$	$\frac{2}{1 - \frac{K}{2D_l^2} + \sqrt{\left(1 - \frac{K}{2D_l^2}\right)^2 + \frac{4K}{D_l^2}}}$	
$G_{vd}(s)$	$G_{d0} / \left(1 + \frac{s}{\omega_p}\right)$			
G_{d0}	$\frac{MV_{\rm IN}}{D_{\rm l}} \cdot \frac{2\left(1-M\right)}{2-M}$	$\frac{MV_{\rm IN}}{D_{\rm I}} \cdot \frac{1 - 2M}{1 - M}$	$\frac{MV_{\rm IN}}{D_{\rm 1}} \cdot \frac{2(1-M)(2M-1)}{1-2(M-1)^2}$	
ω_p	$\frac{1}{R_{\rm OUT}C} \cdot \frac{2-M}{(1-M)}$	$\frac{1}{R_{\rm OUT}C} \cdot \frac{2(1-M)}{(1-2M)}$	$\frac{1}{R_{\rm OUT}C} \cdot \frac{1 - 2(M-1)^2}{(2M-1)(1-M)}$	

Table III. Values of M and control-to-output transfer function $G_{vd}(s)$ of 2-level and 3-level buck converters in CCM

	2-Level CCM	3-Level CCM Buck Converter[5,6]		
	Buck Converter[9]	<i>M</i> <0.5	<i>M</i> >0.5	
$M = \frac{V_{\rm OUT}}{V_{\rm IN}}$	D	D_1	$D_1 + \frac{1}{2}$	
$G_{vd}(s)$	$\frac{G_{d0} / \left(1 + \frac{s}{Q \omega_0} + \frac{s^2}{\omega_0^2}\right)}{V_{\rm IN}}$ $\frac{1}{\sqrt{LC}}$ $R_{\rm OUT} \sqrt{\frac{C}{L}}$			
G_{d0}				
$\omega_{_0}$				
Q				

$$G_{d0} = j_2 \left(R_{\rm OUT} \, \| \, r_2 \right) \tag{15}$$

$$\omega_p = \frac{1}{\left(R_{\text{OUT}} \parallel r_2\right)C}.$$
 (16)

Note that in the equivalent circuit, the inductor is in series with the input current source; thus, the inductor dynamics (particularly at low frequencies) is assumed insignificant [8]. In Table II, values of M and the control-to-output transfer function of the 2-level [8] and 3-level buck converters in the DCM are summarized. For comparison, Table III shows values of M and the transfer function of the 2-level [9] and 3-level [5-6] buck converters in the CCM. Similar to conventional 2-level buck

converters, 3-level buck converters in the DCM have a firstorder lag transfer function, while 3-level buck converters in the CCM have a second-order lag transfer function, suggesting that different controllers are required for the DCM and CCM. In the design of a conventional 2-level buck converter operating both in the CCM for heavy loads (e.g., 100mA) in the active mode and in the DCM for light loads (e.g., 0.1mA) in the stand-by mode, the controller must be carefully designed for combined operation in the CCM and DCM [10]. The proposed model enables the design of controllers for 3-level buck converters in the DCM.

IV. VALIDATION OF DERIVED TRANSFER FUNCTION BY TIME-DOMAIN SMALL-SIGNAL-INJECTION SIMULATION

The derived transfer function is verified by time-domain small-signal-injection simulation. In the behavioral model of a 3-level buck converter on the MATLAB/Simulink platform, a small-amplitude sinusoidal signal at a given frequency is injected into the input of a PWM modulator. The response of the 3-level buck converter at the frequency is obtained by comparison of the amplitude and phase shift between the input signal and output signal. The orthogonal demodulation scheme is employed to calculate the gain and phase characteristics [11]. Whole-frequency characteristics of the transfer function are acquired by sweeping the frequencies of the injected signals. Since C_{FLY} does not exist in the derived transfer function, an ideal DC voltage of half of V_{IN} is used instead of C_{FLY} . Note that in this case, a negligible ripple voltage on C_{FLY} is assumed. Fig. 7 shows Bode plots of the derived transfer function in Table II and simulation results under the conditions of f_{SW} =50MHz, L=56nH, C=10nF, $R_{OUT}=30\Omega$, $V_{IN}=5V$, $D_1=0.25V$, M=0.275. The Bode plot of the derived transfer function is consistent with that of the simulated results, which indicates the validity of the derived transfer function of the 3-level buck converter in the DCM.

V. CONCLUSION

In this paper, the modeling of 3-level buck converters in the DCM was conducted. The transfer function was derived by using an averaged switch model and verified by time-domain small-signal-injection simulation. It was revealed that 3-level buck converters in the DCM have a first-order lag transfer function, while 3-level buck converters in the CCM have a second-order lag transfer function, suggesting that different controllers are required for the DCM and CCM, similar to conventional 2-level buck converters.

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Fig. 7. Bode plots of derived theoretical model and time-domain simulation results of (a) gain and (b) phase under conditions of f_{SW} =50MHz, L=56nH, C=10nF, R_{OUT} =30 Ω , V_{IN} =5V, D_1 =0.25, M=0.275.

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